



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/053,760	01/22/2002	Srinivis Podila	002-4: CSN 4836	5445

30080 7590 04/04/2005

LAW OFFICE OF CHARLES E. KRUEGER
P.O. BOX 5607
WALNUT CREEK, CA 94596-1607

EXAMINER

KIM, HONG CHONG

ART UNIT	PAPER NUMBER
----------	--------------

2186

DATE MAILED: 04/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

**UNITED STATES DEPARTMENT OF COMMERCE****U.S. Patent and Trademark Office**

Address: COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.
---------------------------------	-------------	---	---------------------

EXAMINER

ART UNIT	PAPER
----------	-------


20050401

DATE MAILED:

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner for Patents

Enclosed is missing page in the previous office action which sent out on 11/26/04.


HONG CHONG KIM
PRIMARY EXAMINER

1 4. (Currently amended) A computer program product for use on a system
2 including a digital computer that implements shared memory shared between at least a first
3 and second process, the computer program product comprising:
4 a computer usable medium having computer readable program code physically
5 embodied therein, said computer program product further comprising:
6 computer readable program code for causing said digital computer to maintain
7 access information identifying all client processes having access to shared ~~shared~~ memory;
8 computer readable program code for causing said digital computer to map the
9 access information into private address space of the first process;
10 computer readable program code, responsive to a peer mapping system call
11 from said first process, for causing said digital computer to utilize the access information to
12 map the additional shared memory into the address space of the second process having access
13 to the shared memory so that second process has the entire active shared memory mapped to
14 its address space.

1 5. (Original) The computer program product of claim 4 further comprising:
2 computer readable program code for causing said digital computer to maintain
3 information specifying the location of shared memory mapped to each process.

122
4/1/05

1 6. (Currently amended) The computer program product of claim 4 further
2 comprising:
3 computer readable program code for causing said digital computer to maintain
4 a data structure containing the process IDs of each process having access to the shared
5 memory.

1 7. (Currently amended) The computer program product of claim 4 further
2 comprising:
3 computer readable program code for causing said digital computer to:
4 maintain a data structure containing descriptive names of each process having
5 access to the shared memory;
6 maintain a data structure containing the process IDs of each process having
7 access to the shared memory; and
8 map the descriptive name of each process to a process ID for the process.